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Docket No.: 60188-156

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: Customer Number: 20277

Masahiro FUKUI et al.

: Confirmation Number: 1135

Serial No.: 10/085,009

: Group Art Unit: 2818

~~Filed: March 1, 2002~~

~~Examiner: Quoc Hoang~~

For: WIRING METHOD IN LAYOUT DESIGN OF SEMICONDUCTOR
INTEGRATED CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT AND
FUNCTIONAL MACRO

REQUEST FOR RECONSIDERATION

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 31, 2003, having a three-month shortened statutory period for response set to expire on October 31, 2003, reconsideration of the above-identified application is respectfully requested in view of the following remarks.

Enclosed herein are corrected drawings which label Figures 17-19 as prior art.

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TC 2800 MAIL ROOM